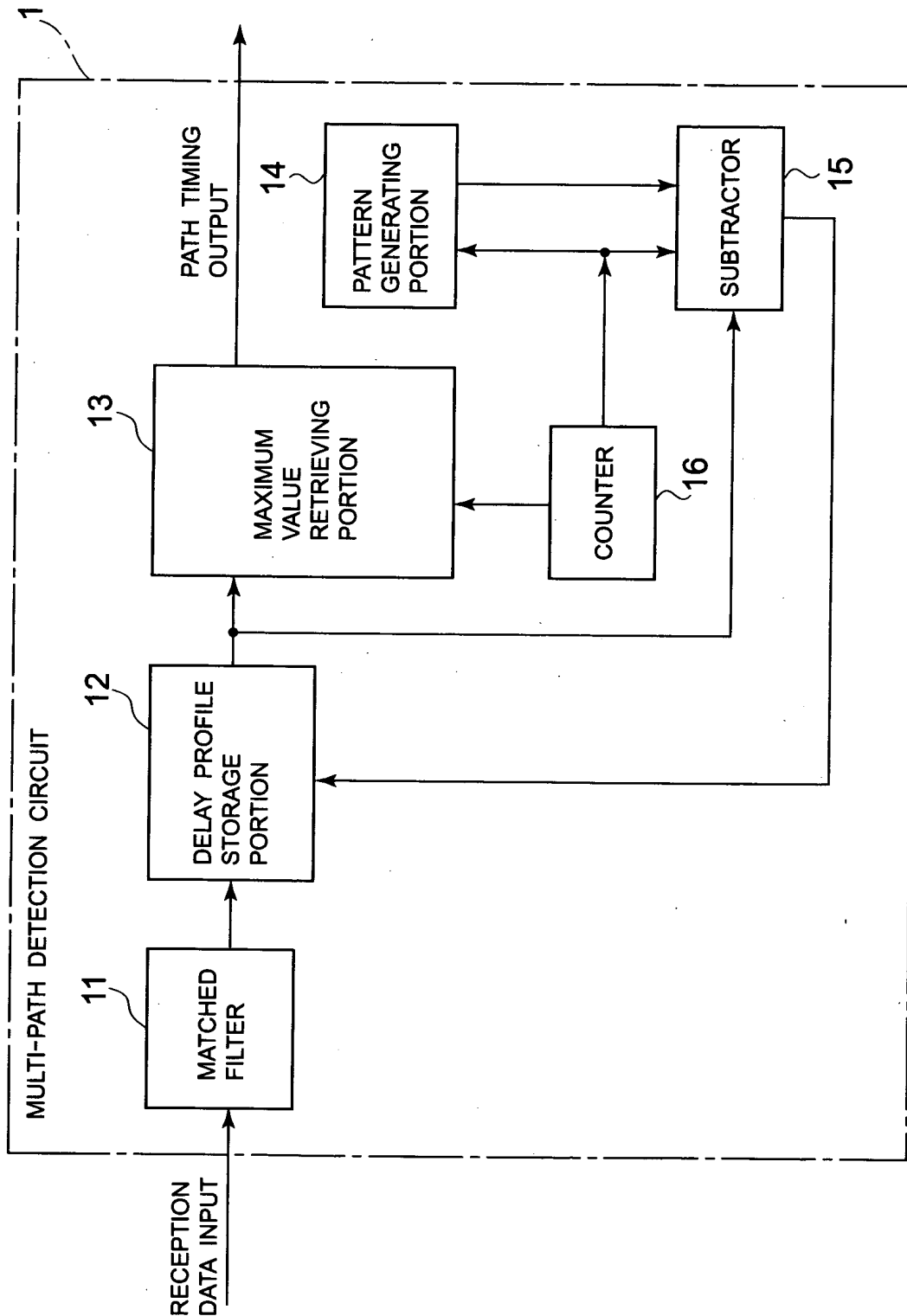


FIG. 1



Title: PATTERN GENERATION CIRCUIT
MULTI-PATH DETECTION CIRCUIT
EMPLOYING THE SAME AND MULTI-
PATH DETECTION METHOD
Inventor(s): Michihiro OHSUGE
DOCKET NO.: 053969/0125

FIG. 2

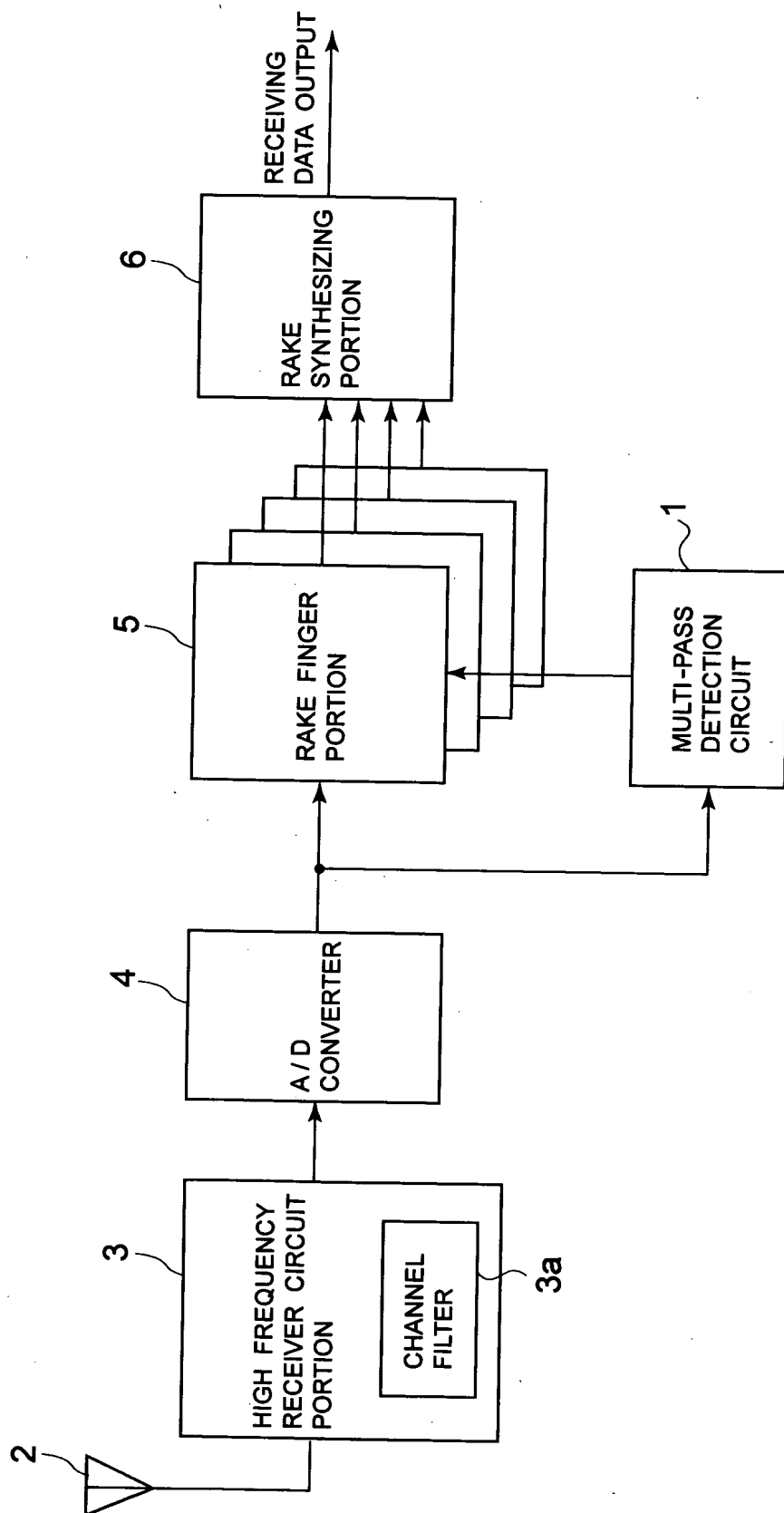


FIG. 2

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FIG. 3

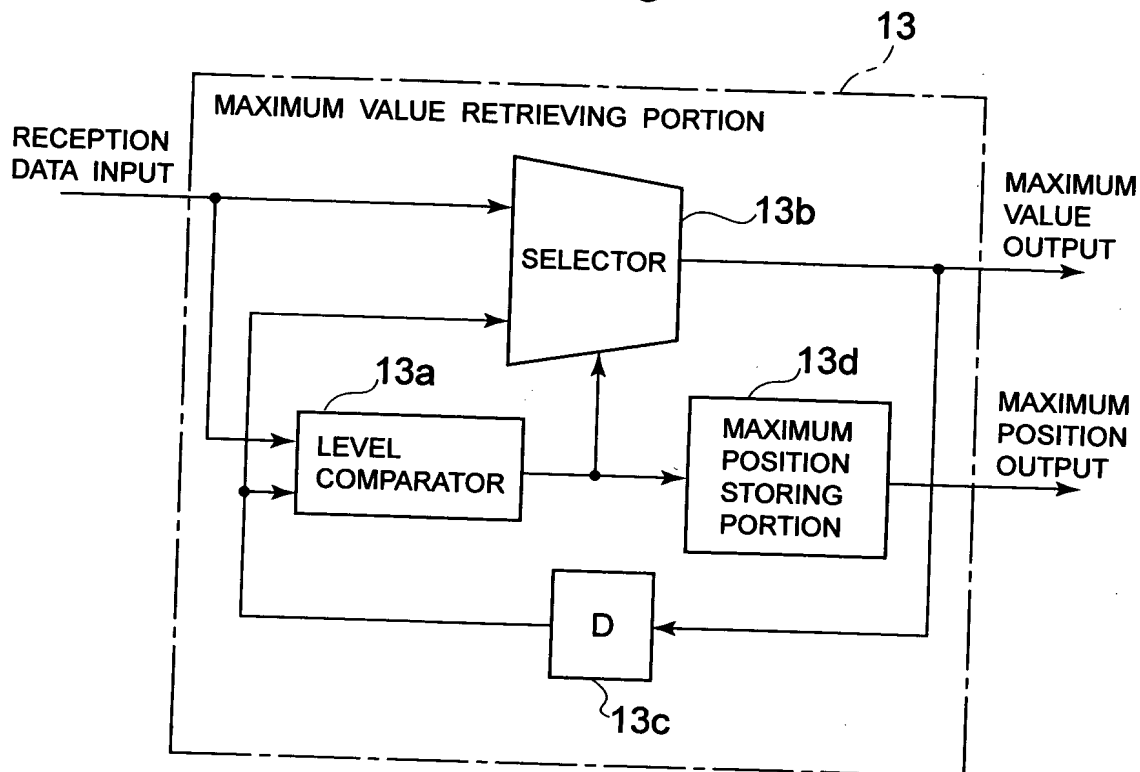
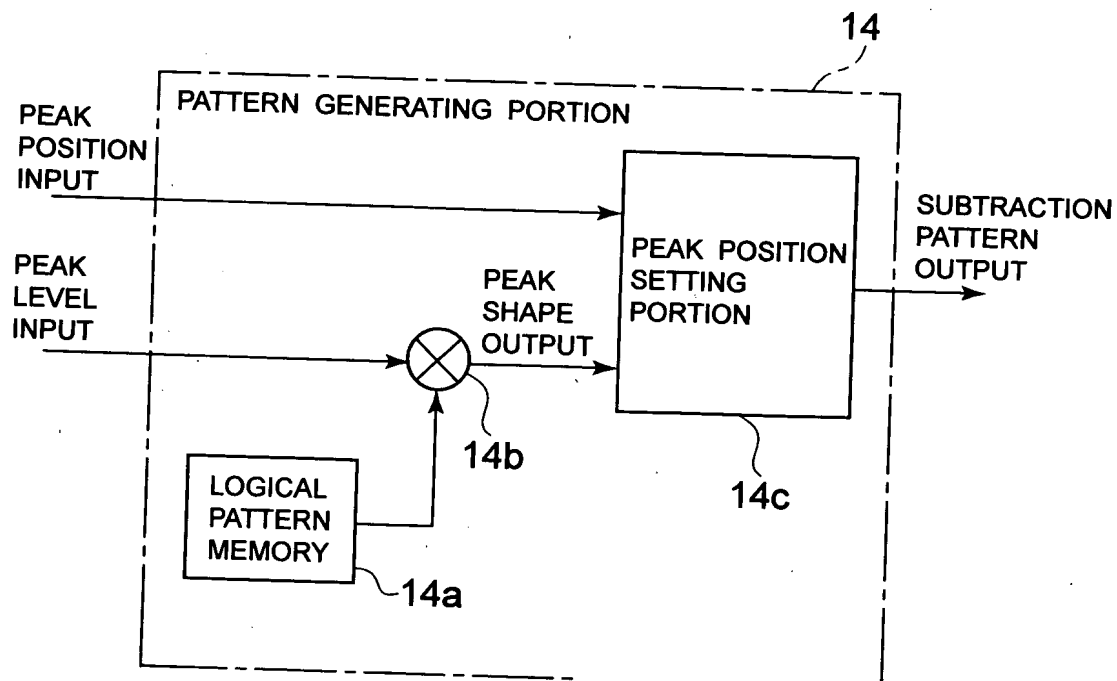


FIG. 4



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FIG. 5A

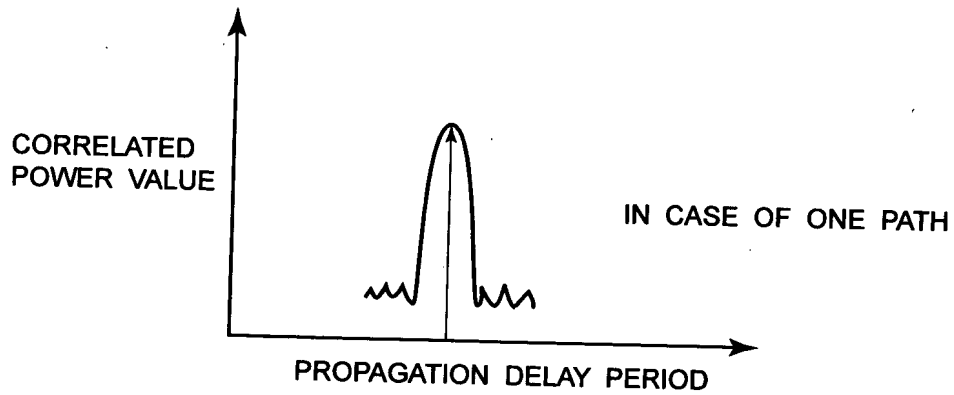


FIG. 5B

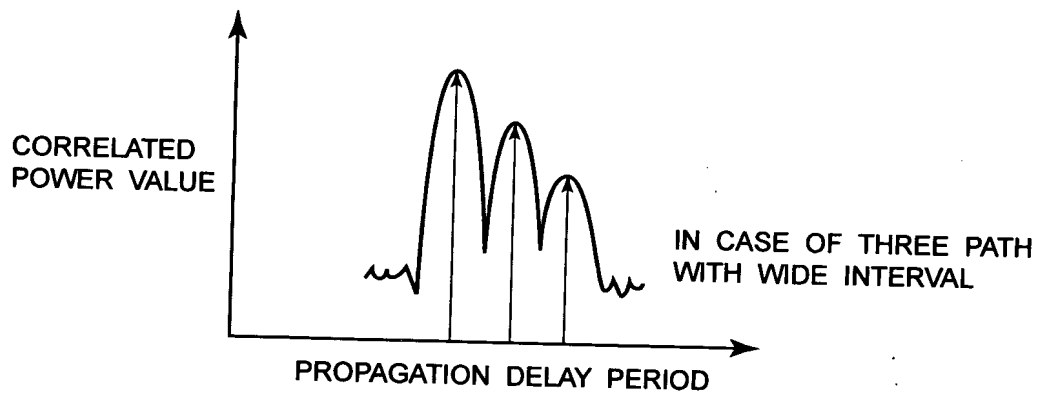
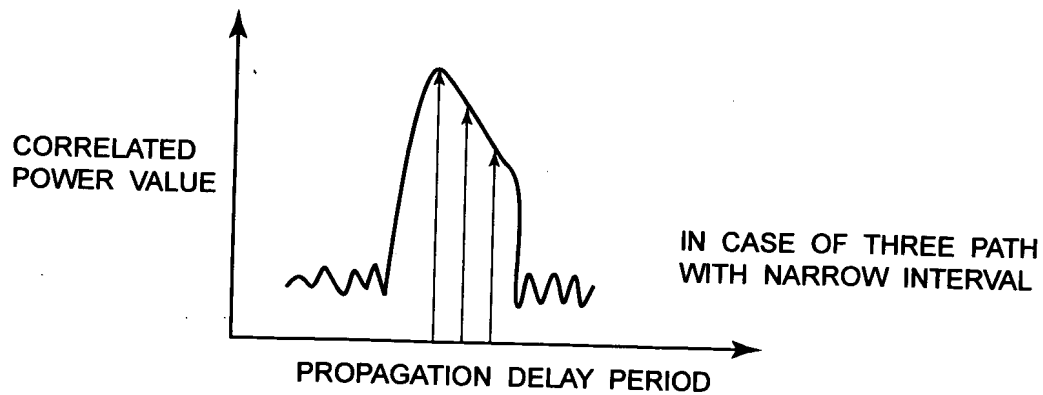


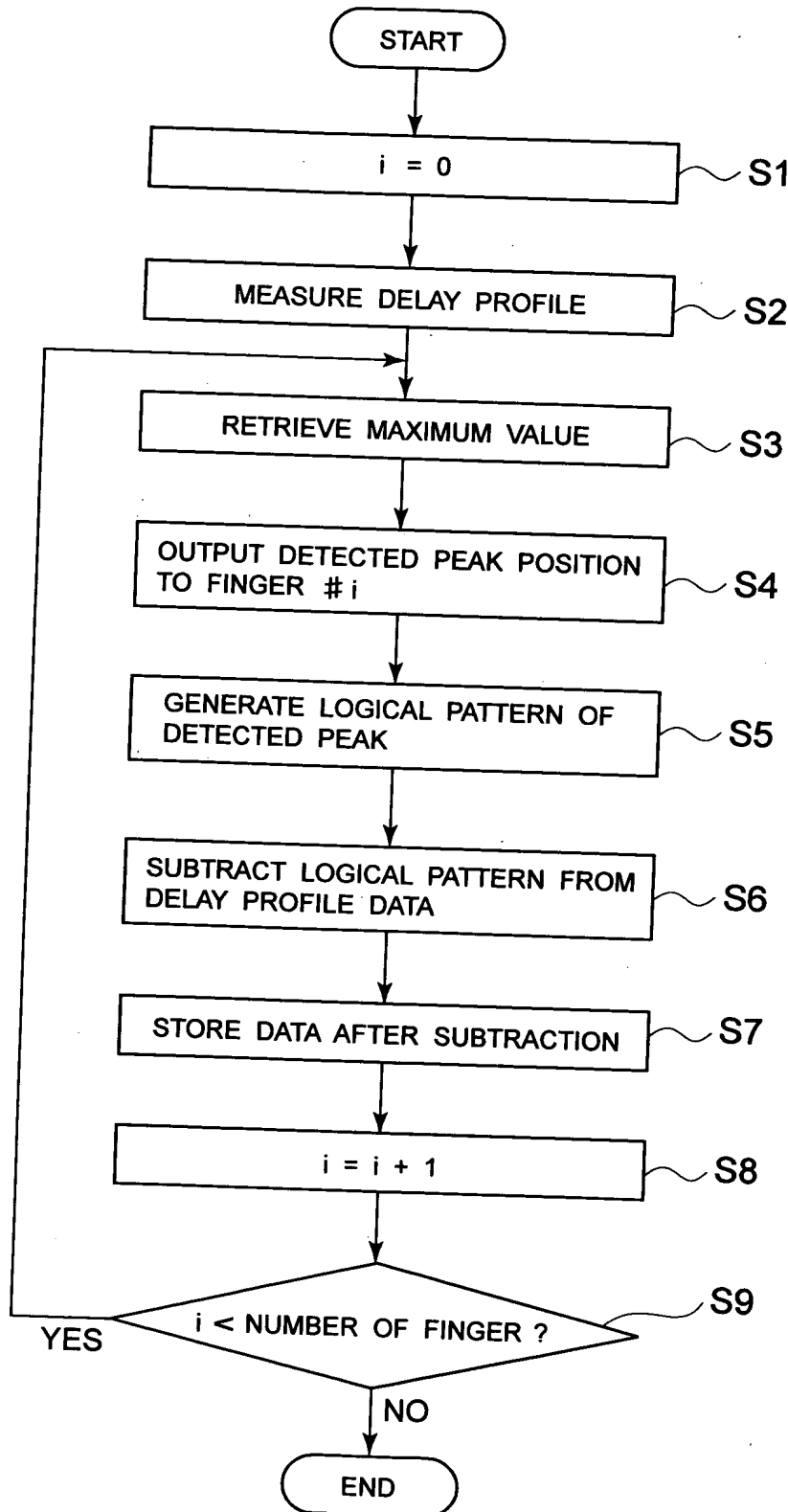
FIG. 5C



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FIG. 6



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T062T0-90502260

FIG. 7A

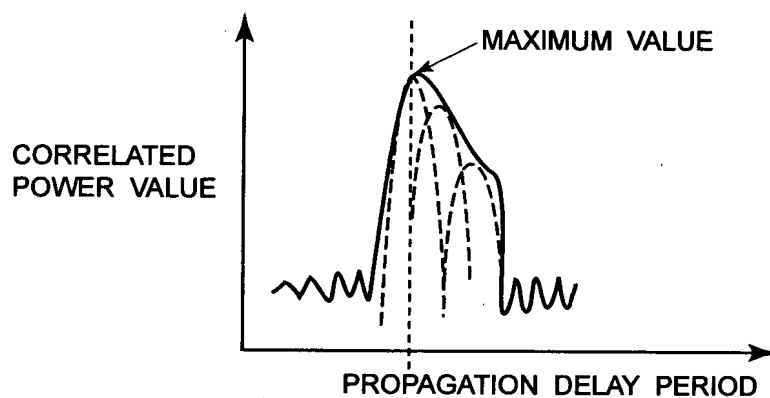


FIG. 7B

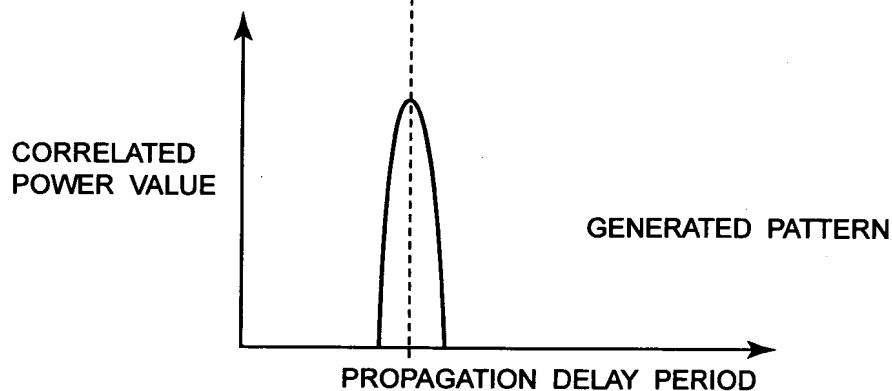
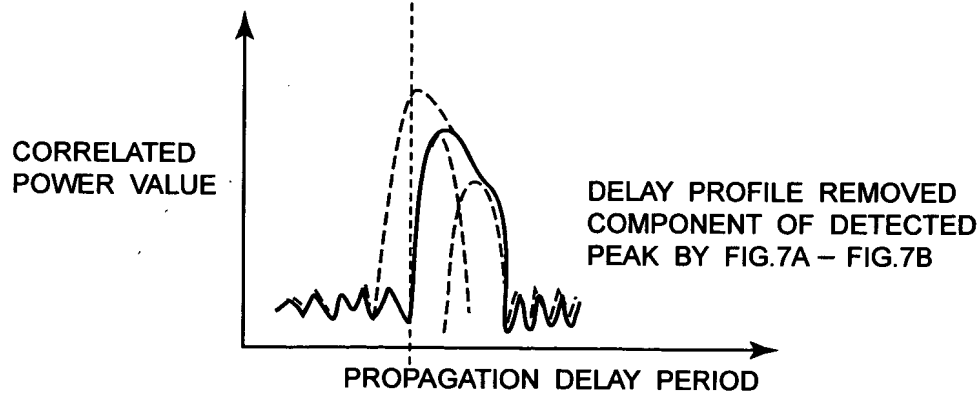


FIG. 7C



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T062T0-90507260

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FIG. 8

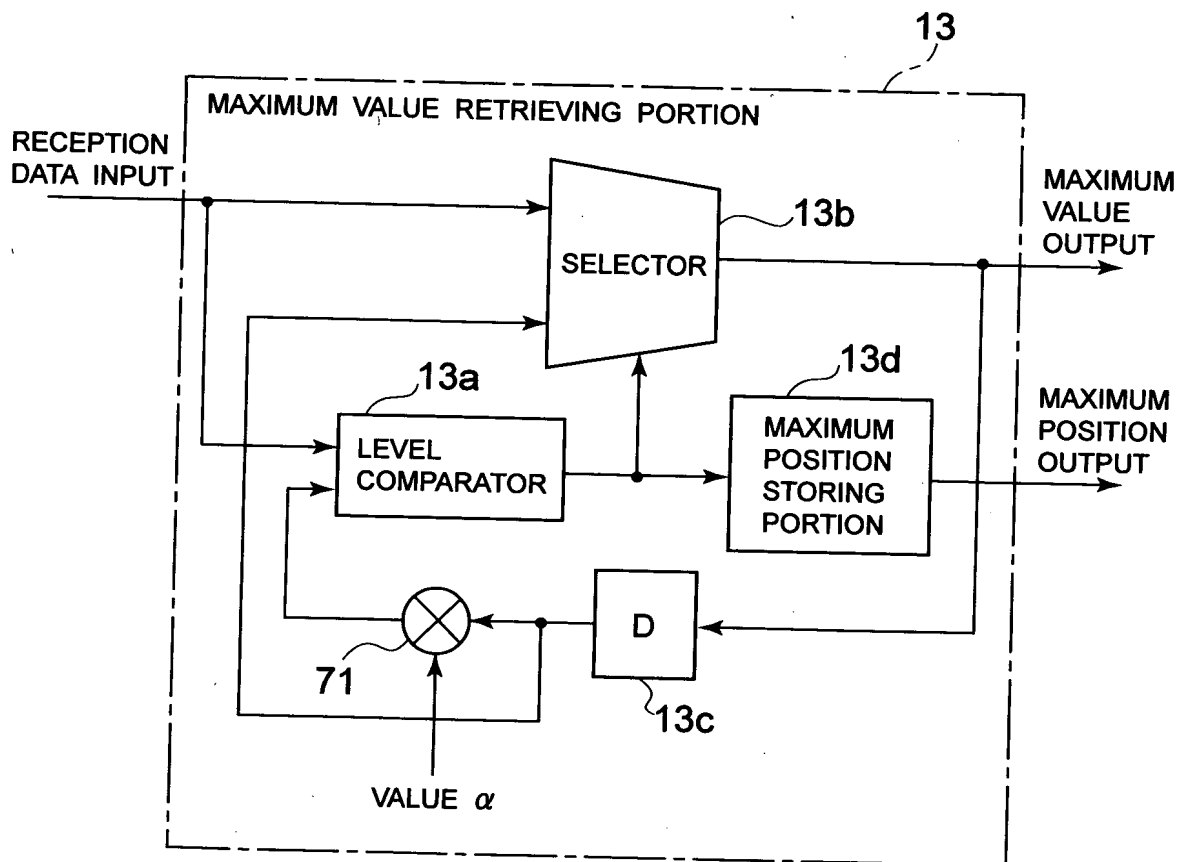
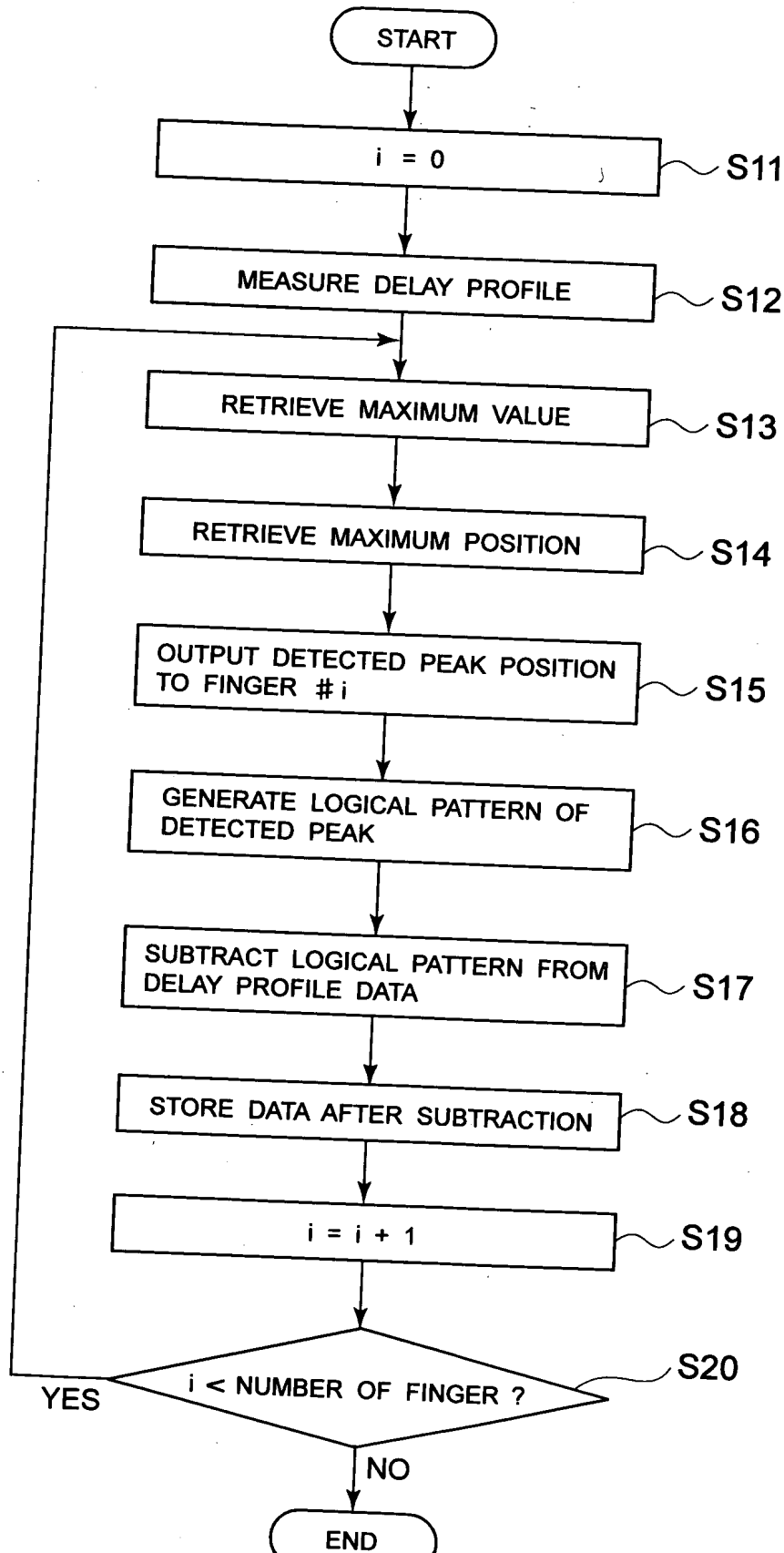


FIG. 9



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FIG. 10A

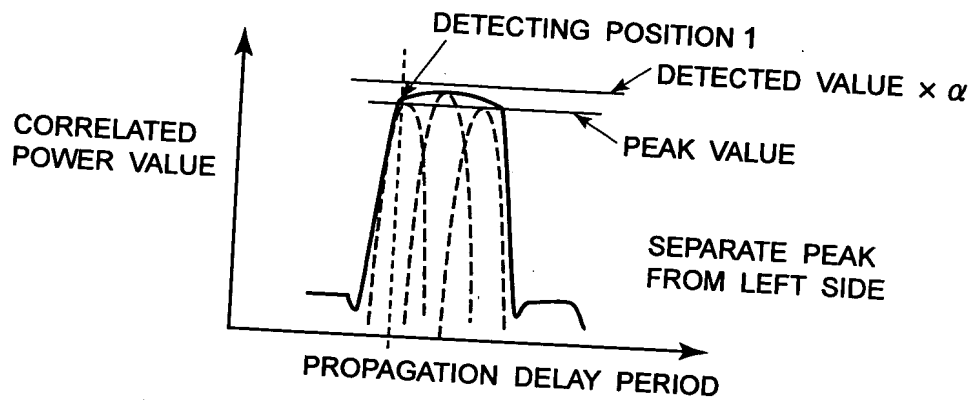


FIG. 10B

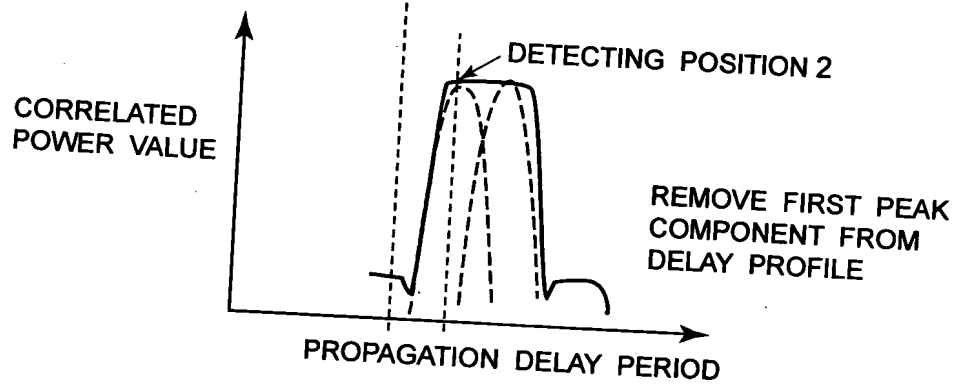
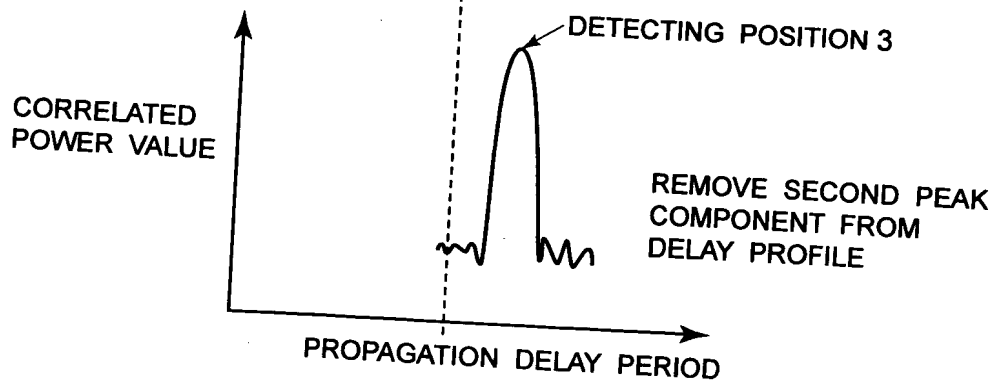


FIG. 10C



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FIG. 11

FIG. 11

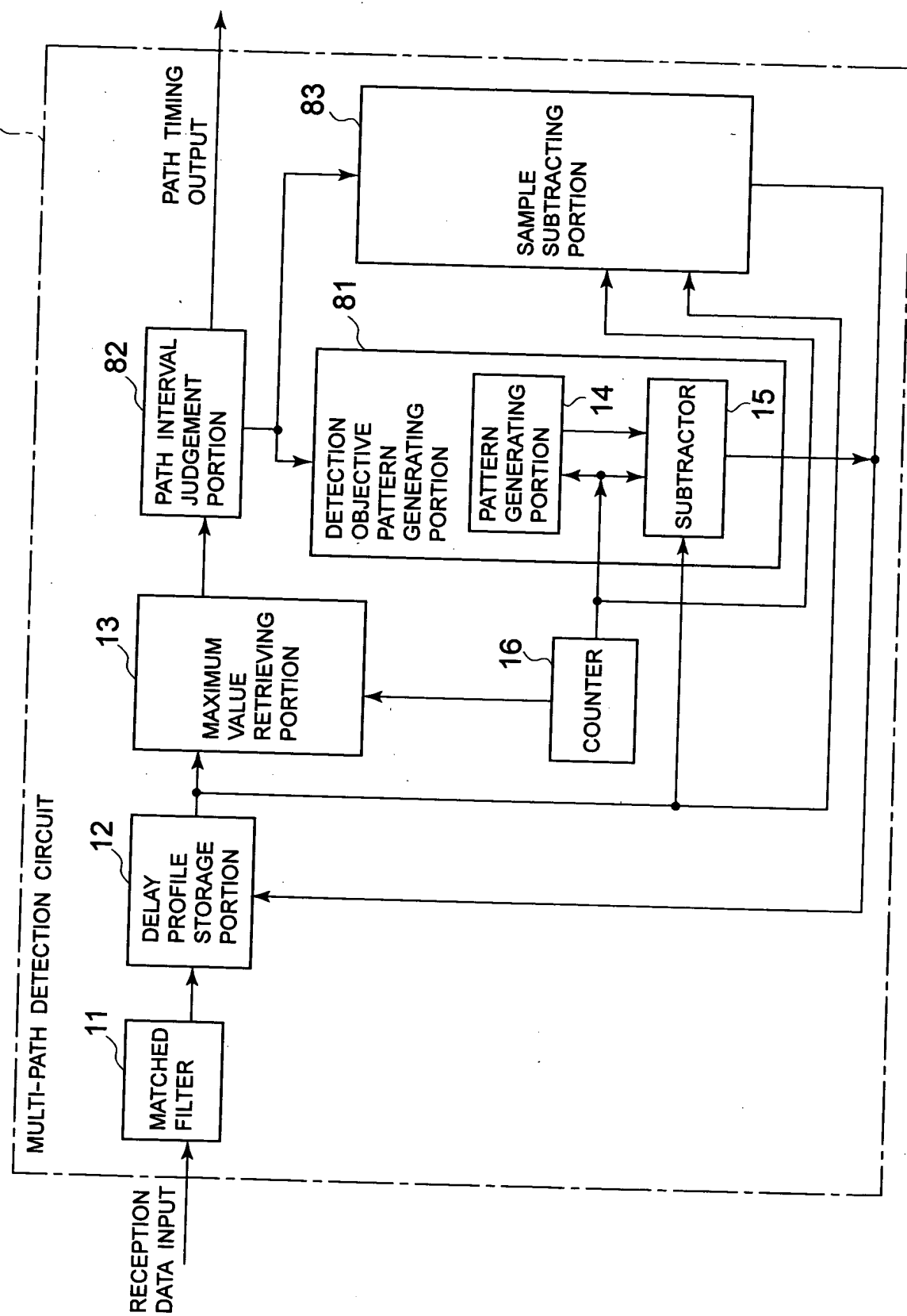
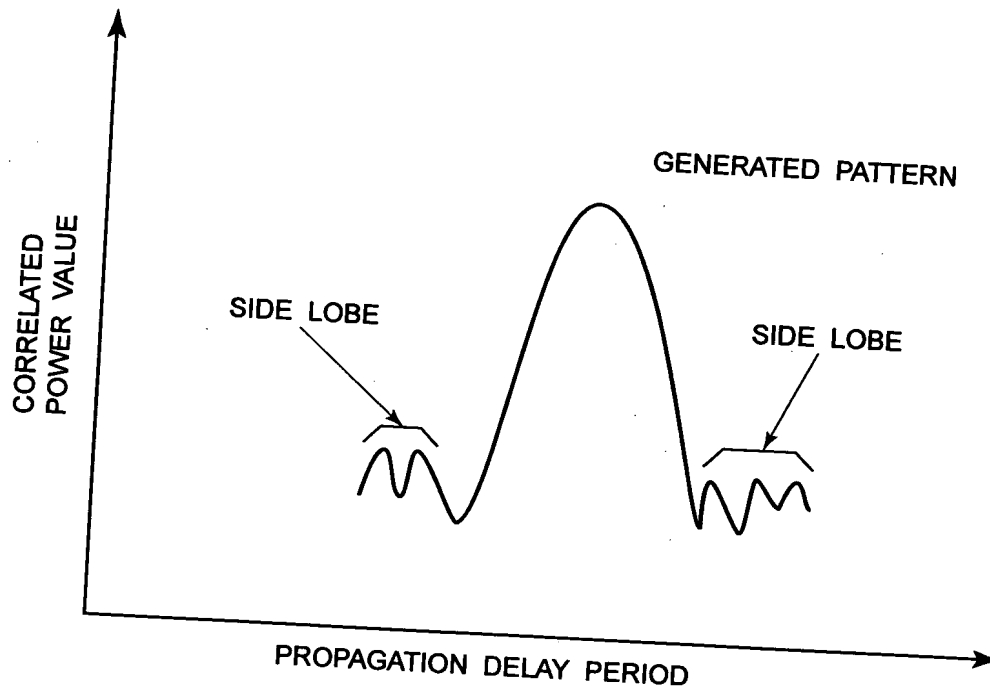


FIG. 12



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